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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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Application Number

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First Named Inventor

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Art Unit

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Examiner Name

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U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS

Examiner Initials	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-			

Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		JP 9-245081	9/19/97	Nippon Telegraph & Telephone Corp.		Abstract
		JP 8-249372	9/27/96	Toshiba Corp.		Abstract
		JP 8-153127	6/11/96	Hitachi Ltd.		Abstract

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶
		Notice of Grounds for Rejection issued by the Japanese Patent Office mailed April 11, 2006, for Japanese Patent Application No. 11-264538, and English-language translation thereof	
		UCHINO, "METHOD FOR ESTIMATING POWER CONSUMPTION OF AN INTEGRATED CIRCUIT," Toshiba Technology Publications (August 29, 1996), 14-46:1-2	

Examiner Signature	<i>Ferris</i>	Date Considered	5/24/06
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